Practitioner's Docket No. 14912.833

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Stephen E. Savas)
Serial No.: 09/997,158) Group No.: 3614 1763
Filed: 11/28/2001) Examiner: Luz L. Alejandro Mulero)
For: Systems and Methods for Enhancing Plasma Processing of a Semiconductor Substrate)
Commissioner for Patents Washington, D.C. 20231	
RESI	PONSE
In response to the Examiner's Action dated	d 10/07/2002, applicant elects Group I, claims 1-16
drawn to an apparatus, without traverse.	
Please cancel claims 17-21 without prejud	lice.
	e any additional fees which may be required,
including petition fees and extension of time fees,	
No. 14912.833).	
Respectfu WILSON	Ily submitted, SONSINI GOODRICH & ROSATY RECEIVED JAN 0 9 2003
January 7, 2003 Date Michael J. 1 Reg. No. 37	GROUP 3600
Customer No. 021971 650 Page Mill Road Palo Alto, CA 94304-1050 (650) 493-9300	TATAL CROWN AND AND AND AND AND AND AND AND AND AN
CERTIFICATE OF FACSIMILE TRANSMISSION	
DATE OF TRANSMISSION: January 7, 2003 FACSIMILE TELEPHONE: (703) 872-9310 I hereby certify that this correspondence is being facsimile transmitted transmitted from the factorial factorial from the factorial factori	nitted to the U.S. Patent and Trademark Office on the DATE OF

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